10/648,276 **Application Number** REQUEST **FOR** Confirmation Number 4435 **CONTINUED EXAMINATION (RCE)** August 27, 2003 Filing Date TRANSMITTA First Named Inventor Shinya WATANABE MAIL STOP RCE 2813 Group Art Unit JUN 0 6 2005 Commissioner for Patents P.O. Box 1450 Jennifer M. DOALN **Examiner Name** Alexandria, VA 22313-1450 Q76956 Matter Number Subsection (b) of 35 U.S.C. § 132, effective state Semiconductor Chip Having an Arrayed provides for continued examination of an utility or Waveguide Grating and Method of plant application filed on or after June 8, 1995 Title Manufacturing the Semiconductor Chip and... This is a Request for Continued Examination (RCE) under 37 C.F.R. § 1.114 of the above-identified application. 1. SUBMISSION REQUIRED UNDER 37 C.F.R. § 1.114 a. Previously submitted i. Please enter and consider the amendment(s)/reply under 37 C.F.R. § 1.116 previously filed on ii. Consider the arguments in the Appeal Brief or Reply Brief previously filed on iii.□ Other b. \(\overline{\mathbb{D}}\) Enclosed i. 🗹 Amendment/Reply ii. □ Affidavit(s)/Declaration(s) iii.□ Information Disclosure Statements (IDS) Petition for Extension of Time iv. 🗹 v. Other 2. MISCELLANEOUS a. Suspension of action on the above-identified application is requested under 37 C.F.R. § 1.103(c) for a period of _____ months b. Other 3. FEES A check for the RCE statutory fee of \$790.00 is attached. The USPTO is directed and authorized to charge all required fees, except for the Issue Fee and the Publication Fee, to Deposit Account No. 19-4880. Please also credit any overpayments to said Deposit Account. A duplicate copy of this transmittal letter is attached. **CORRESPONDENCE ADDRESS** Direct all correspondence to the address for SUGHRUE MION, PLLC filed under the Customer Number listed below: WASHINGTON OFFICE 06/08/2005 HALI11 00000036 10648276 23373 790,00 OF CUSTOMER NUMBER 01 FC:1801

SIGNATURE OF ATTORNEY

Name

Signature

John M. Bird

Registration No. 46,027

Date June 6, 2005

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PATENT APPLICATION

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of

Docket No: Q76956

Shinya WATANABE, et al.

Appln. No.: 10/648,276

Confirmation No.: 4435

Group Art Unit: 2813

Filed: August 27, 2003

Examiner: Jennifer M. DOALN

For:

SEMICONDUCTOR CHIP HAVING AN ARRAYED WAVEGUIDE GRATING AND METHOD OF MANUFACTURING THE SEMICONDUCTOR CHIP AND MODULE

CONTAINING THE SEMICONDUCTOR CHIP

AMENDMENT UNDER 37 C.F.R. § 1.114(c)

MAIL STOP RCE

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

Sir:

In response to the Office Action dated January 5, 2005, please amend the aboveidentified application as follows on the accompanying pages.

TABLE OF CONTENTS

AMENDMENTS TO THE CLAIMS	2
REMARKS	5